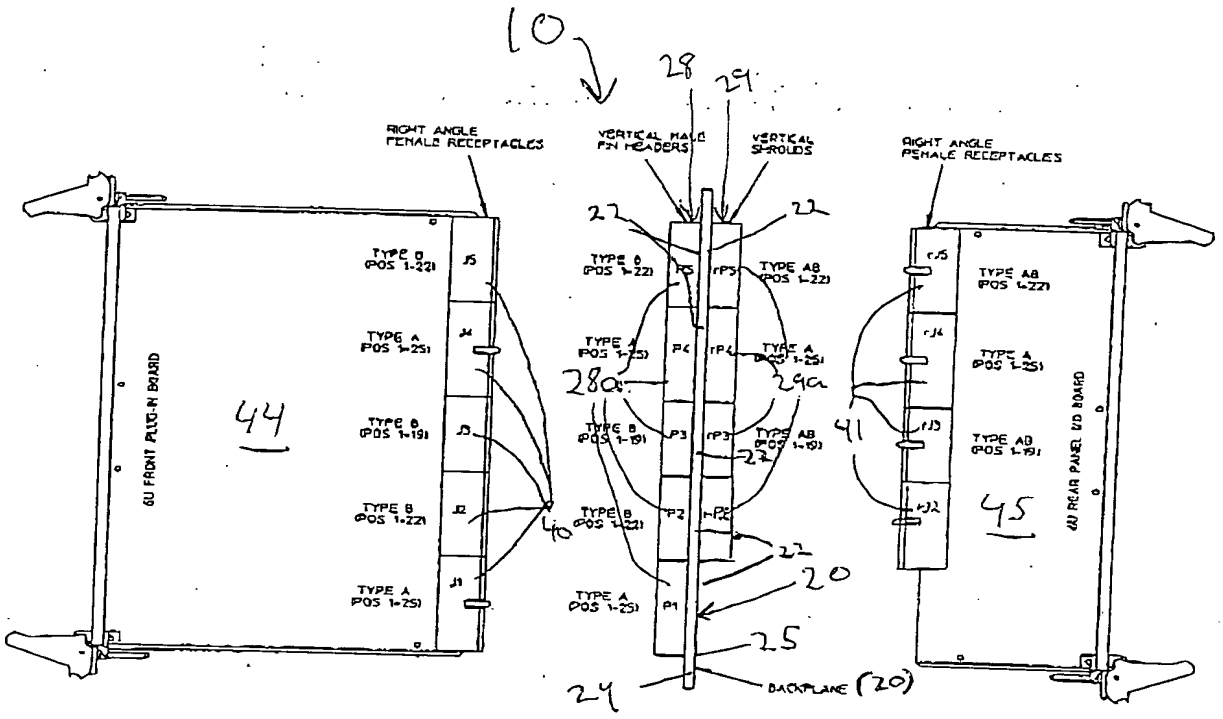
10
28
29
RIGHT ANGLE FEMALE RECEPTACLES
VERTICAL MALE PIN HEADERS
VERTICAL SHROUDS
RIGHT ANGLE FEMALE RECEPTACLES
22
TYPE D (POS 1-22)
TYPE AB (POS 1-22)
TYPE A (POS 1-25)
TYPE A (POS 1-25)
280
29a
TYPE B (POS 1-19)
TYPE AB (POS 1-19)
41
TYPE B (POS 1-22)
40
20
TYPE A (POS 1-25)
25
24
BACKPLANE (20)
TYPE AB (POS 1-22)
TYPE A (POS 1-25)
TYPE AB (POS 1-19)
6U FRONT PLUG-IN BOARD
44
6U REAR PANEL I/O BOARD
45

Fig. 1a

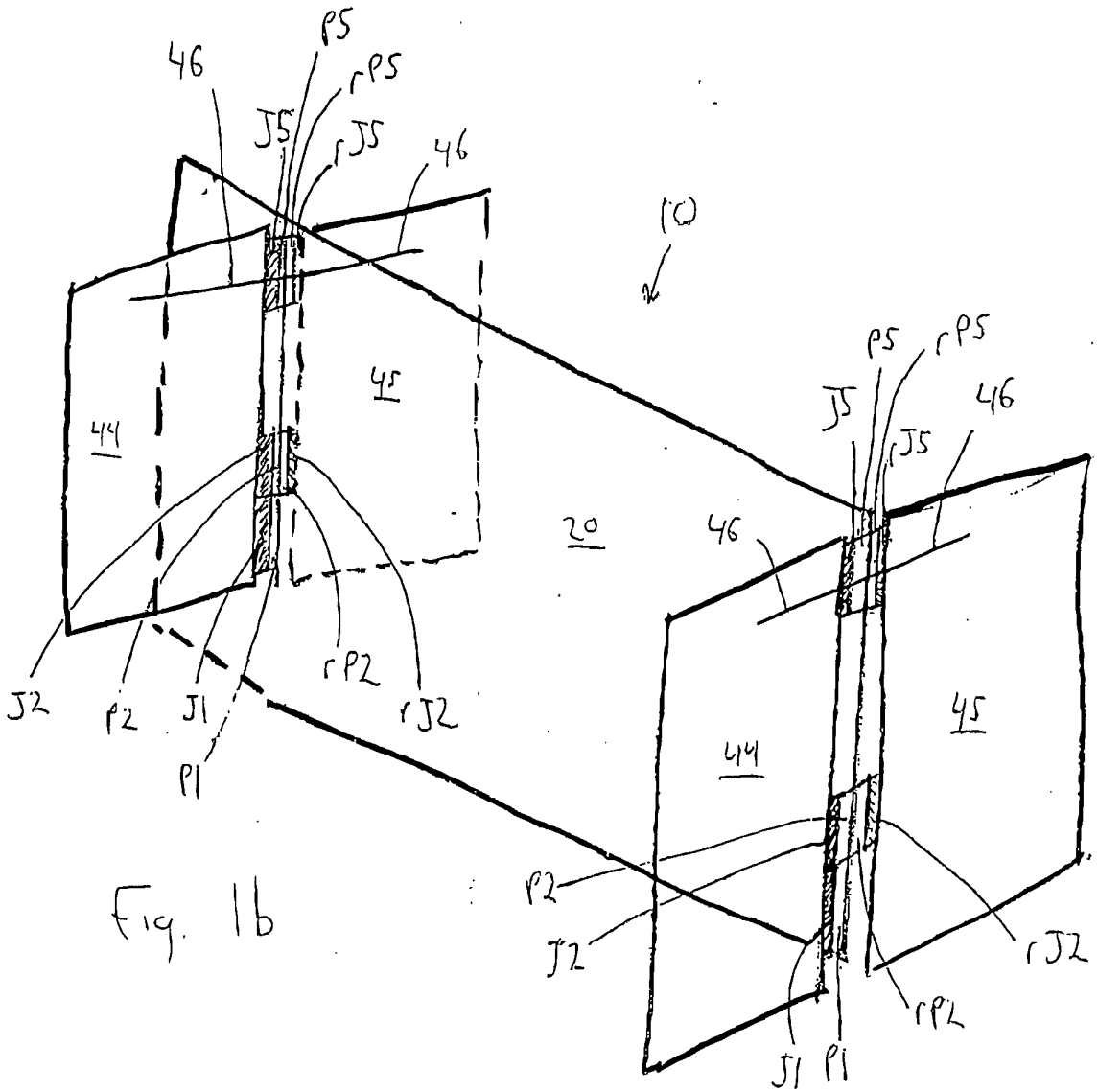
46
P5
rP5
J5
rJ5
46
10
P5
rP5
J5
rJ5
46
44
45
20
46
J2
P2
J1
P1
rP2
rJ2
44
45
P2
J2
rJ2
rP2
J1
P1

Fig. 1b

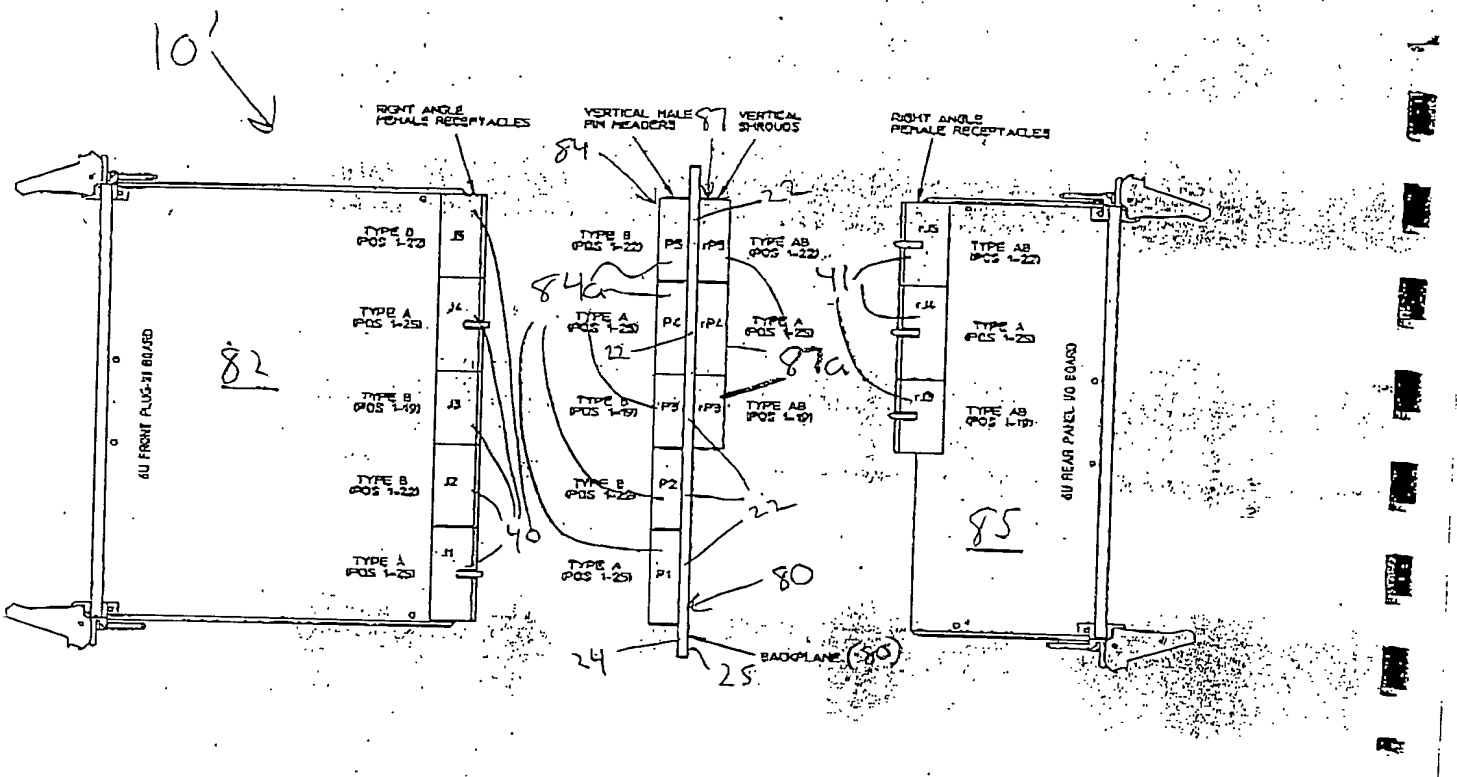
10'
RIGHT ANGLE FEMALE RECEPTACLES
VERTICAL MALE PIN HEADERS
VERTICAL STRAIGHT
RIGHT ANGLE FEMALE RECEPTACLES
84
84a
82
6U FRONT PLUG-IN BOARD
TYPE D (POS 1-27)
TYPE A (POS 1-25)
TYPE B (POS 1-19)
TYPE B (POS 1-22)
TYPE A (POS 1-25)
J5
J4
J3
J2
J1
TYPE B (POS 1-22)
TYPE AB (POS 1-22)
TYPE A (POS 1-25)
TYPE A (POS 1-25)
TYPE D (POS 1-19)
TYPE AB (POS 1-19)
TYPE B (POS 1-22)
TYPE A (POS 1-25)
P5
P4
P3
P2
P1
rP5
rP4
rP3
rP3
22
40
80
24
25
BACKPLANE (60)
41
87a
TYPE AB (POS 1-22)
TYPE A (POS 1-25)
TYPE AB (POS 1-19)
rJ5
rJ4
rJ3
85
6U REAR PANEL I/O BOARD

Fig. 2a

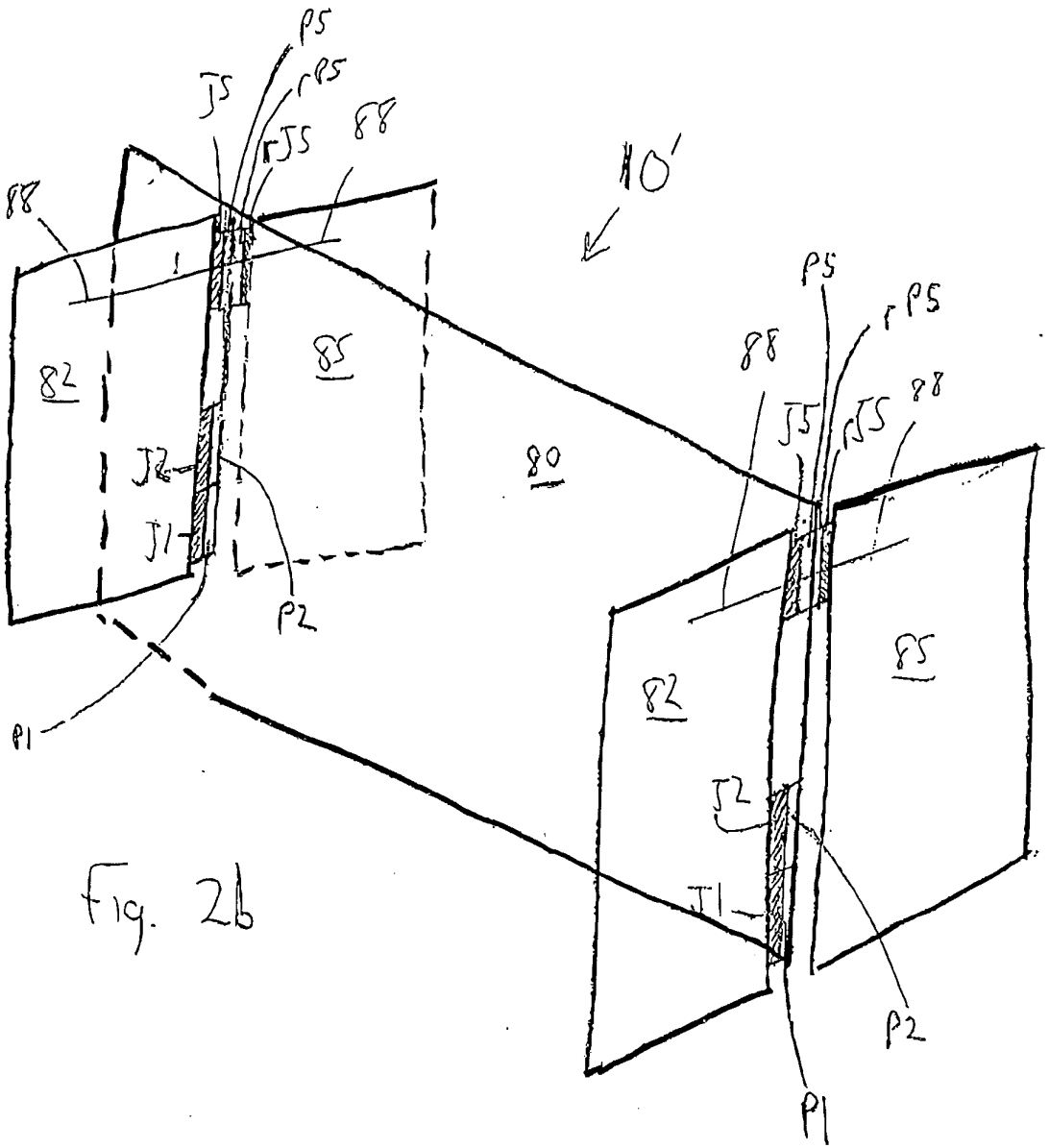

Fig. 2b

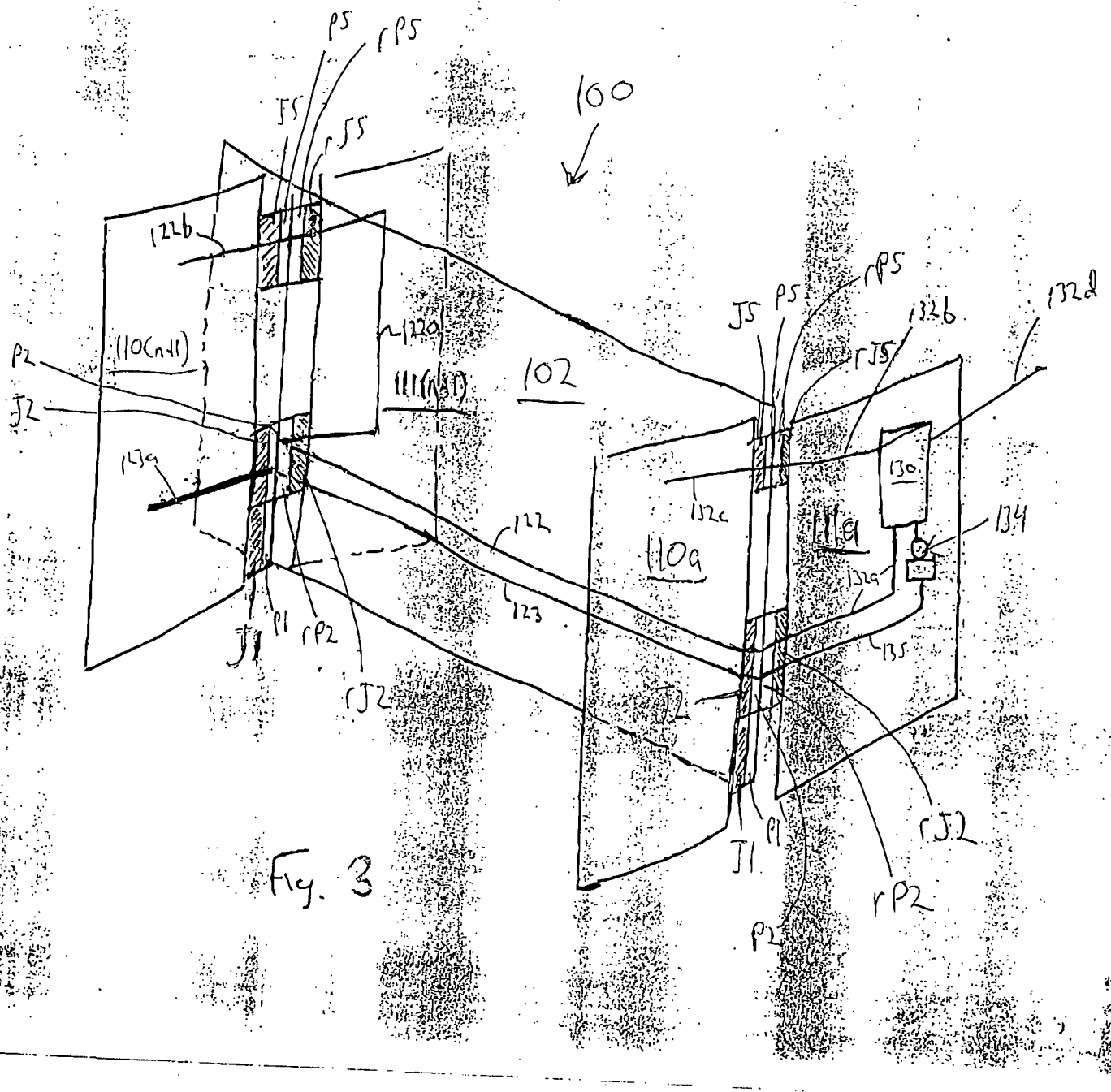
100
102
110a
110(n+1)
111(n+1)
122b
122
123
123a
132b
132c
132d
134
135
130
P5
rP5
J5
rJ5
P2
J2
rJ2
P1
rP2
J1

Fig. 3

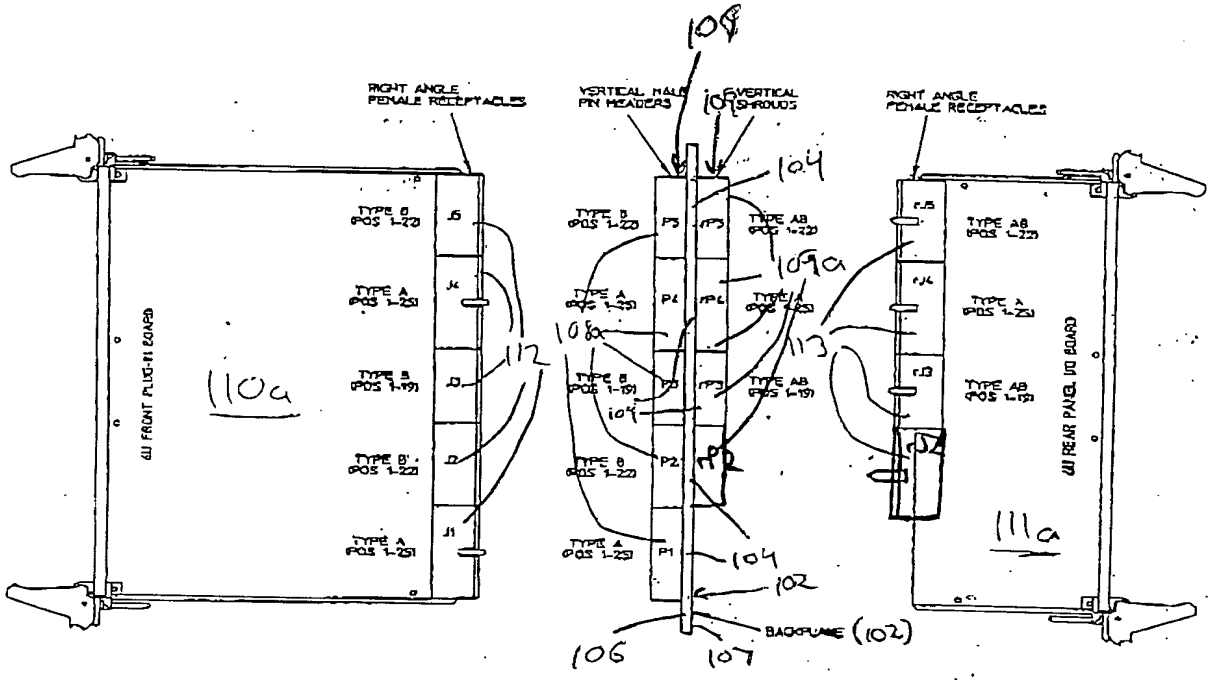
RIGHT ANGLE FEMALE RECEPTACLES
VERTICAL MALE PIN HEADERS
VERTICAL SHROUDS
RIGHT ANGLE FEMALE RECEPTACLES
108
104
109a
108a
113
112
110a
111a
102
106
107
BACKPLANE (102)
4U FRONT PLUG-IN BOARD
4U REAR PANEL I/O BOARD

Fig. 4

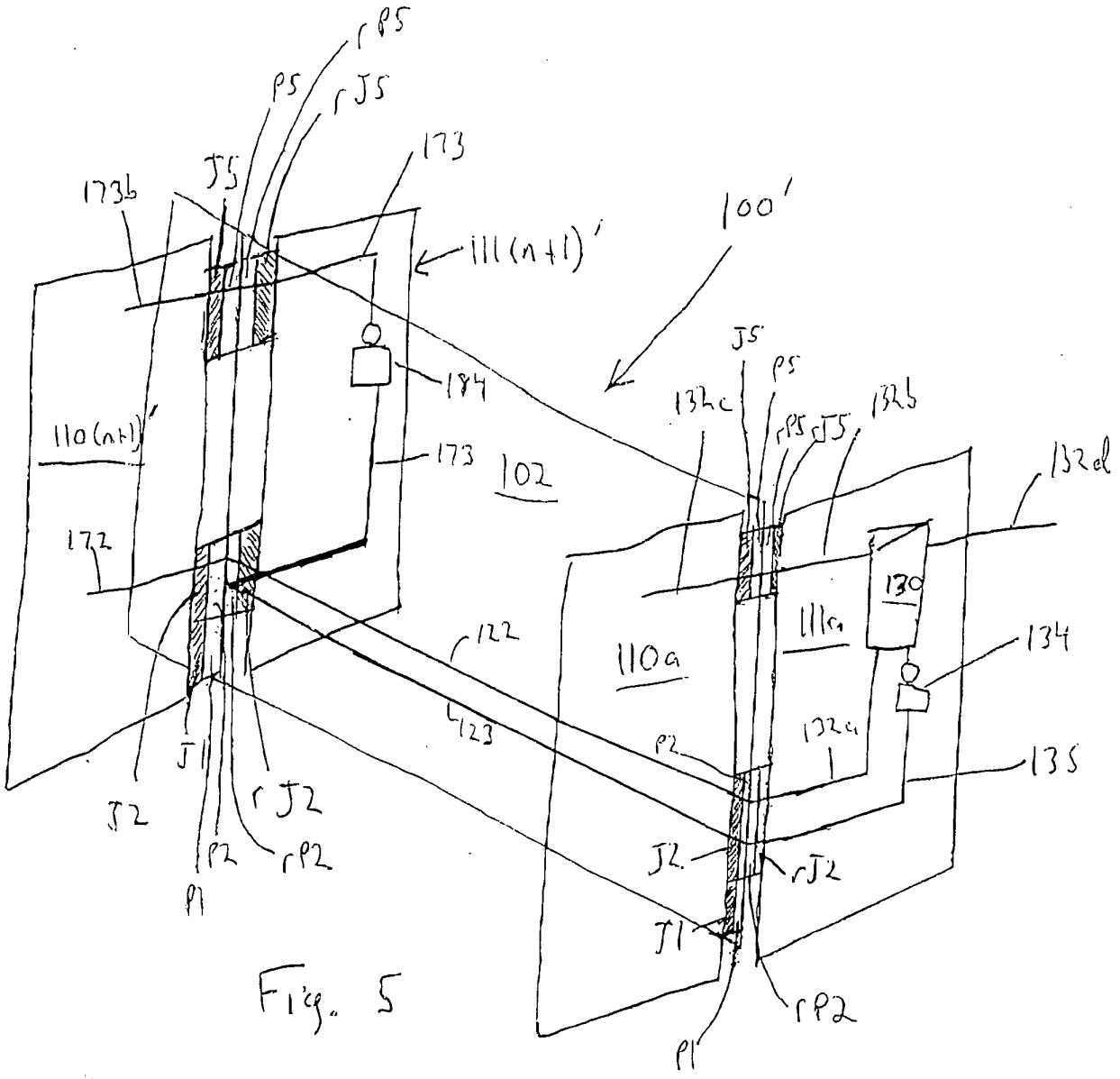
rP5
P5
rJ5
173
173b
J5
111(n+1)'
100'
184
110(n+1)'
173
102
172
122
123
J1
J2
rJ2
P2
rP2
P1
J5'
P5
132c
rP5
rJ5'
132b
132d
130
111a
110a
134
132a
135
P2
J2
rJ2
J1
P1
rP2

Fig. 5

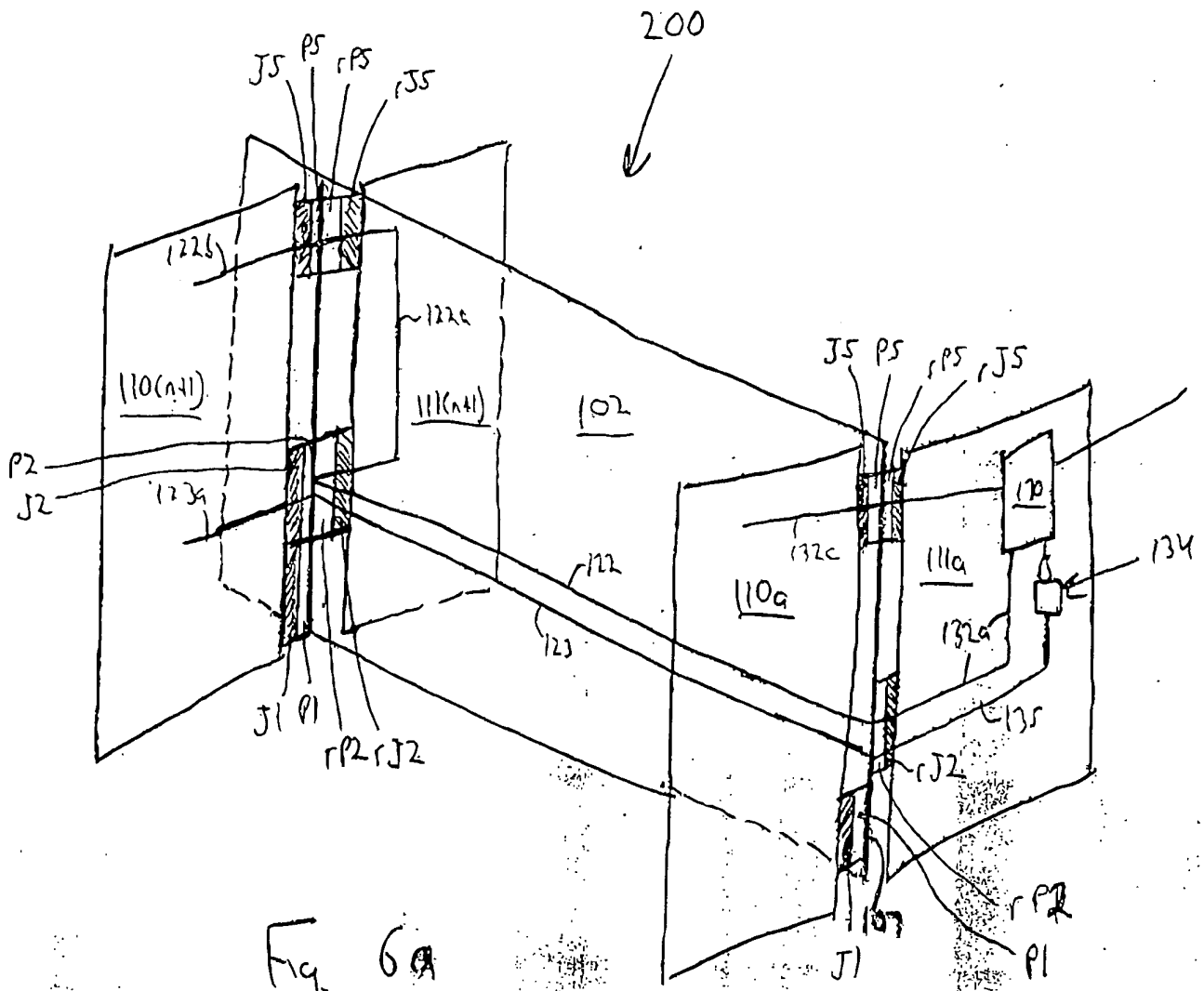
200
J5
P5
rP5
rJ5
122b
122a
110(n+1)
111(n+1)
102
P2
J2
123
122
123
J1
P1
rP2
rJ2
J5
P5
rP5
rJ5
170
132c
111a
110a
134
132a
135
rJ2
107
rP2
J1
P1

Fig. 6A

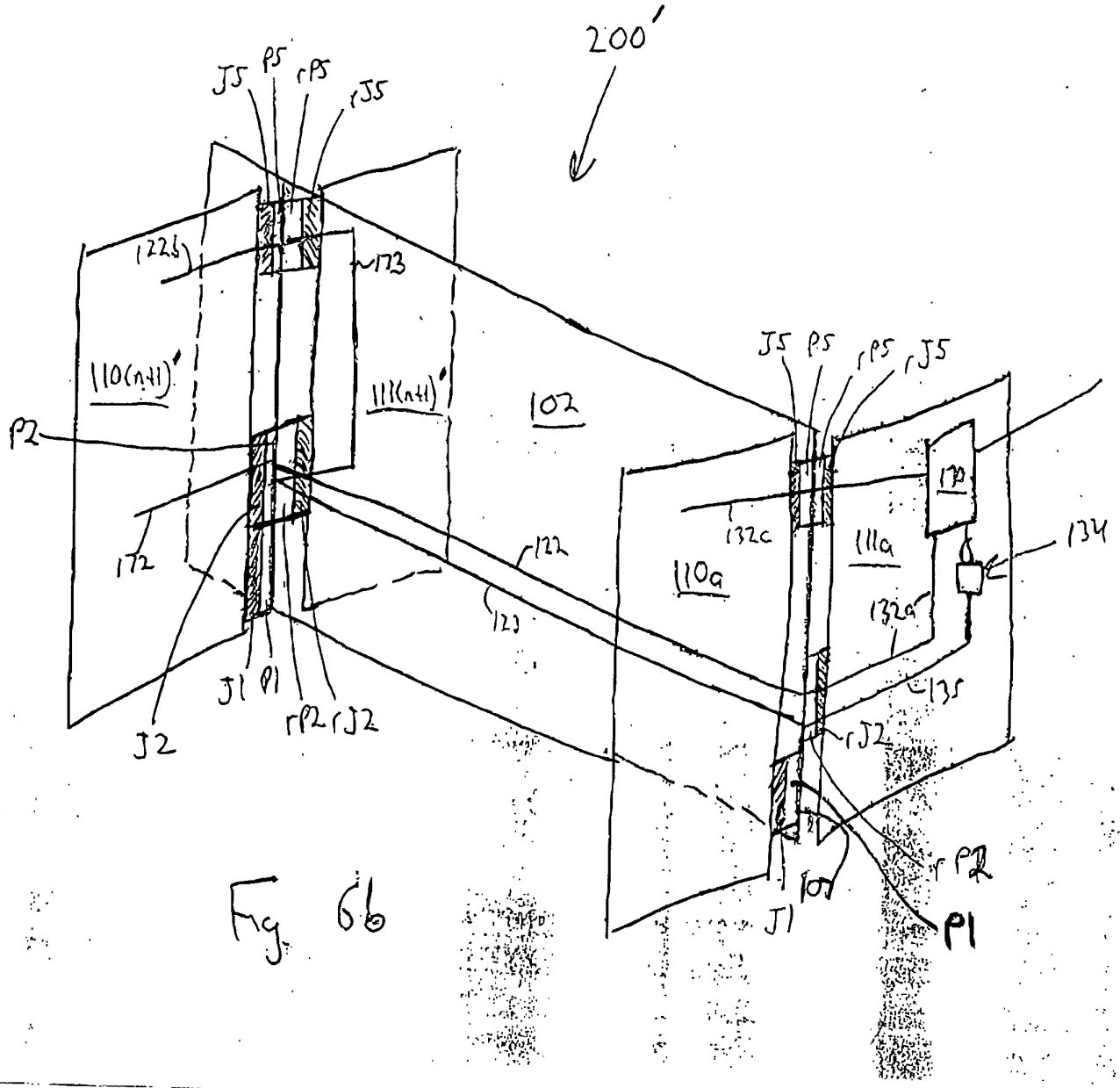
200'
J5
P5
rP5
rJ5
122b
173
110(n+1)'
111(n+1)'
102
P2
172
122
123
110a
111a
132c
132a
134
135
J1
P1
rP2
rJ2
J2
107
P2
P1

Fig. 6

TABLE 1

Pin Assignments

Table 13. CompactPCI Peripheral Slot 64-Bit Connector Pin Assignments[1][10, 11]

22	GND	GA4[13]	GA3[13]	GA2[13]	GA1[13]	GA0[13]	GND	
21	GND	RSV	RSV	RSV	RSV	RSV	GND	
20	GND	RSV	RSV	RSV	GND	RSV	GND	
19	GND	RSV	RSV	RSV	RSV	RSV	GND	P2
18	GND	BRSVP2A18	BRSVP2B18	BRSVP2C18	GND	BRSVP2E18	GND	/
17	GND	BRSVP2A17	GND	RSV	RSV	RSV	GND	J2
16	GND	BRSVP2A16	BRSVP2B16	RSV	GND	BRSVP2E16	GND	
15	GND	BRSVP2A15	GND	RSV	RSV	RSV	GND	
14	GND	AD[35]	AD[34]	AD[33]	GND	AD[32]	GND	
13	GND	AD[38]	GND	V(I/O)[2]	AD[37]	AD[36]	GND	C
12	GND	AD[42]	AD[41]	AD[40]	GND	AD[39]	GND	O
11	GND	AD[45]	GND	V(I/O)[2]	AD[44]	AD[43]	GND	N
10	GND	AD[49]	AD[48]	AD[47]	GND	AD[46]	GND	N
9	GND	AD[52]	GND	V(I/O)[2]	AD[51]	AD[50]	GND	E
8	GND	AD[56]	AD[55]	AD[54]	GND	AD[53]	GND	C
7	GND	AD[59]	GND	V(I/O)[2]	AD[58]	AD[57]	GND	T
6	GND	AD[63]	AD[62]	AD[61]	GND	AD[60]	GND	O
5	GND	C/BE[5]#	GND	V(I/O)[2]	C/BE[4]#	PAR64	GND	R
4	GND	V(I/O)[2]	BRSVP2B4	C/BE[7]#	GND	C/BE[6]#	GND	
3[3]	GND	RSV	GND	RSV	RSV	RSV	GND	
2[3]	GND	RSV	RSV	UNC[4]	RSV	RSV	GND	
1[3]	GND	RSV	GND	RSV	RSV	RSV	GND	
25	GND	5V	REQ64#	ENUM#	3.3V	5V	GND	
24	GND	AD[1]	5V	V(I/O)[2]	AD[0]	ACK64#	GND	
23	GND	3.3V	AD[4]	AD[3]	5V	AD[2]	GND	
22	GND	AD[7]	GND	3.3V	AD[6]	AD[5]	GND	P1
21	GND	3.3V	AD[9]	AD[8]	M66EN[5]	C/BE[0]#	GND	/
20	GND	AD[12]	GND	V(I/O)[2]	AD[11]	AD[10]	GND	J1
19	GND	3.3V	AD[15]	AD[14]	GND	AD[13]	GND	
18	GND	SERR#	GND	3.3V	PAR	C/BE[1]#	GND	
17	GND	3.3V	IPMB_SCL	IPMB_SDA	GND	PERR#	GND	
16	GND	DEVSEL#	GND	V(I/O)[2]	STOP#	LOCK#	GND	C
15	GND	3.3V	FRAME#	IRDY#	BD_SEL#[7]	TRDY#	GND	O
12-14				KEY AREA				N
11	GND	AD[18]	AD[17]	AD[16]	GND	C/BE[2]#	GND	N
10	GND	AD[21]	GND	3.3V	AD[20]	AD[19]	GND	E
9	GND	C/BE[3]#	IDSEL[7]	AD[23]	GND	AD[22]	GND	C
8	GND	AD[26]	GND	V(I/O)[2]	AD[25]	AD[24]	GND	T
7	GND	AD[30]	AD[29]	AD[28]	GND	AD[27]	GND	O
6	GND	REQ#	GND	3.3V	CLK	AD[31]	GND	R
5	GND	BRSVP1A5	BRSVP1B5	RST#	GND	GNT#	GND	
4	GND	IPMB_PWR	HEALTHY#[14]	V(I/O)[2]	INTP	INTS	GND	
3	GND	INTA#	INTB#	INTC#	5V	INTD#	GND	
2	GND	TCK[15]	5V	TMS[16]	TDO[15]	TDI[15]	GND	
1	GND	5V	-12V	TRST#[16]	+12V	5V	GND	
Pin	Z[14]	A	B	C	D	E	F[9]	

= long pins (front only) = short pins (front only) = medium length pins (front only)

Fig. 7